

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Phase Change Memory Cell On Silicon-On Insulator Substrate																																																																						
<p>Application Number :</p> <p>Confirmation Number:</p> <p>First Named Applicant: Stephen Furkay</p> <p>Attorney Docket Number: BUR920030147US1</p> <p>Art Unit:</p> <p>Examiner:</p> <p>Search string: (6597009 or 6593176 or 6590807 or 6586761 or 6576921 or 6579318 or 5177717 or 20030128646 or 20030128568 or 20030123277 or 20030067013 or 20020144811).pn</p>																																																																							
<h3>US Patent Documents</h3> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>6597009</td><td>2003-07-22</td><td>Wicker</td><td></td><td></td><td></td></tr><tr><td></td><td>2</td><td>6593176</td><td>2003-07-15</td><td>Dennison</td><td></td><td></td><td></td></tr><tr><td></td><td>3</td><td>6590807</td><td>2003-07-08</td><td>Lowrey</td><td></td><td></td><td></td></tr><tr><td></td><td>4</td><td>6586761</td><td>2003-07-01</td><td>Lowrey</td><td></td><td></td><td></td></tr><tr><td></td><td>5</td><td>6576921</td><td>2003-06-10</td><td>Lowrey</td><td></td><td></td><td></td></tr><tr><td></td><td>6</td><td>6579318</td><td>2003-06-10</td><td>Lee, et al</td><td></td><td></td><td></td></tr><tr><td></td><td>7</td><td>5177717</td><td>1993-01-05</td><td>Sato, et al</td><td></td><td></td><td></td></tr></tbody></table>								init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	6597009	2003-07-22	Wicker					2	6593176	2003-07-15	Dennison					3	6590807	2003-07-08	Lowrey					4	6586761	2003-07-01	Lowrey					5	6576921	2003-06-10	Lowrey					6	6579318	2003-06-10	Lee, et al					7	5177717	1993-01-05	Sato, et al			
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